IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: K. NISHIBASHI et al

Serial No. 10/303,024

Filed: November 25, 2002

Group Art Unit:

2811

Examiner:

C. Nguyen

For:

SEMICONDUCTOR INTEGRATED

CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents Alexandria, VA 22314

Sir:

In accordance with the duty of disclosure, the applicants inform the Examiner of the documents cited during prosecution of the parent application, Serial No. 10/303,024.

The above-referenced patent application is a divisional application of U.S. Serial No. 10/303,024, filed November 25, 2002, from which priority is claimed under 35 U.S.C. § 120.

Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form to indicate that the references have been considered.

Respectfully submitted,

John R. Matti⁄n

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Attorney for Applicant(s)

MATTINGLY, STANGER & MALUR 1800 Diagonal Road, Suite 370 Alexandria, Virginia 22314 (703) 684-1120

Date: January 22, 2004

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. ASA-830-04	SI	ERIAL NO.			
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				U.S. PATENT	DOCUMENTS				
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	AA	5,564,572	8/5/97	Kawase					
	АВ	5,767,546	6/16/98	Williams et al					\Box
	AC	5,541,434	7/30/96	Nicholls et al					
	AD	5,278,436	1/11/94	Asahina et al					
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